



The  
University  
Of  
Sheffield.

Electronic &  
Electrical  
Engineering.

## EEE118      ELECTRONIC DEVICES AND CIRCUITS

**Credits:          20**

### Course Description including Aims

This module introduces the underlying solid-state physical principles that govern the properties of the active and passive circuit components that comprise all electronic and electrical circuits. Issues affecting the practical behaviour of resistors, capacitors and especially diodes and transistors are discussed. The circuit environments in which diodes and transistors are used, and the models describing their internal behaviour and external interactions, are introduced. It is shown how transistors and diodes can be used in both switching circuits and amplifier circuits and the important concept of small signal modelling is introduced in the context of the latter.

The module aims to:

1. describe the key conduction mechanisms - drift and diffusion - in solids and in a vacuum.
2. introduce students to the differences between conductors, semiconductors and insulators.
3. to describe the various technologies of resistor and capacitor manufacture and the relative performance issues associated with them.
4. establish a distinction between mobile charge and space charge in semiconductors and their respective roles in electronic devices.
5. develop in students a thorough understanding of the basic mechanisms of the p-n junction.
6. show students how to use their knowledge of semiconductors to create models that relate physical mechanisms in semiconductors to the terminal characteristics of electronic devices, in particular transistors.
7. introduce the idea of non-linear circuit elements and how to handle them.
8. present piecewise linear models and standard circuit symbols used to represent active circuit elements such as p-n junctions, BJTs, JFETs and MOSFETs.
9. explore the application areas of p-n junction diodes and examine their behaviour in a range of circuit contexts.
10. introduce the notion of electronic switches and the application of BJT and MOSFET devices as switches in various circuit environments.
11. introduce the idea of amplification and explain the need for biasing and the concept and use of small signal modelling.
12. introduce the ideal op-amp and basic op-amp circuits and to examine the effects of finite gain.

### Outline Syllabus

**Electrons in a Vacuum** : force on electron in an electric field, energy, velocity, current and current density. **Electrons in Solids** : transport mechanisms, drift, diffusion. Resistivity of metals and physical origin, temperature coefficient. **Insulators** : breakdown strength, dielectrics and relative permittivity, different types of capacitors and their uses. **Semiconductors** : intrinsic and extrinsic, doping, charge carriers, holes, basic relationships of  $J$  for bulk semiconductors. **PN Junctions** : structure, junction potential, forward bias behaviour, charge injection, diode equation. Idea of space charge, Poisson's equation, internal fields, reverse breakdown mechanisms. Diode characteristics and temperature effects.

**Transistors** : JFETs and MOSFETs, basic mechanisms and characteristics, transconductance. BJT, transport mechanisms, charge control model, characteristics, simple circuits to obtain gain. Simple equivalent circuits. **Basic Diode Behaviour** : large and small signal diode models. **Diode Applications** : rectifiers, capacitor input smoothing, ripple, zener diode regulators. Clipping, clamping, voltage doublers, voltage multipliers. **Transistors** : BJT, JFET and MOSFET characteristics, similarities and differences. **Switching Applications** : on-state and off-state behaviour, drive considerations for BJT and MOSFET, inductive loads and back emf, switching AC power, bridge topologies for motor control. **Amplifier Applications** : amplification, biasing, designing dc conditions, thermal stability. Small signal ideas, generation of simple model ( $g_m$  based), equivalent circuits, coupling and decoupling, mid-frequency examples. **Operational Amplifiers** : advantages of - ideal performance. Basic circuit shapes, idea of feedback, follower circuits, virtual earth circuits, effect of finite gains. Use of superposition to handle multiple source amplifiers.

## Time Allocation

48 lectures, 24 problem classes and 124 hours of independent study

## Recommended Previous Courses

entry qualifications

## Assessment

mid term test (January) - 10%, exam (June) answer 4 questions from 6 in 3 hours - 90%

## Recommended Books

## Objectives

“By the end of the unit, a candidate will be able to”

1. determine the differences of electron motion in a vacuum and in solids (drift and diffusion).
2. outline the properties and uses of metals, semiconductors and insulators.
3. identify the physical processes which are important in semiconductor electronic devices.
4. describe the p - n junction and the concept of electron and hole current.
5. appreciate the use of a diode for the emission and detection of light.
6. identify the physical mechanisms within the JFET and BJT that lead to the transconductance and output characteristics.
7. identify under what conditions a diode will conduct and what its effect will be on the behaviour of the circuit as a whole.
8. design simple capacitor input filtered power supplies, understand the significance of the approximations involved and specify voltage ratings for the components used.
9. predict the behaviour of circuits containing resistors, capacitors and diodes such as voltage doublers, peak detectors and differentiators.
10. discuss the similarities and differences between the characteristic behaviour of BJTs, JFETs and MOSFETS.
11. determine key operational parameters of a simple switching circuit and design simple circuits - including ones with inductive loads - to achieve specified goals.

12. analyse and synthesise the two practically useful bias circuits used in BJT amplifiers.
13. apply small signal model ideas to complete circuits and make quantitative estimates of a circuit's small signal performance.
14. calculate circuit gain for inverting, non-inverting operational and multiple input amplifier circuits for both ideal operational amplifiers and ones with a finite gain.